



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hatsuki KANBAYASHI

Application No.: 10/625,746

Filed: July 24, 2003 Docket No.: 115816

For: SEMICONDUCTOR DEVICES, AND MANUFACTURING METHODS, CIRCUIT

SUBSTRATES AND ELECTRONIC EQUIPMENTS FOR THE SAME

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. The present application was filed or entered the U.S. National Stage of the PCT after June 30, 2003. In accordance with the June 11, 2003, Notice waiving the requirements of 37 C.F.R. §1.98(a)(2)(i), copies of any U.S. patents and patent application publications are not attached.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

Thomas J. Pardini Registration No. 30,411

JAO:TJP/mlo

Date: January 14, 2004

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400 DEPOSIT ACCOUNT USE
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(REV. 8-83)		US Dept. of Commo	ATTY DOCKET NO. 115816			APPLICATION NO. 10/625,746		
INFORMATION DISCLOSURE STATEMENT						10/0	725,140	
(Use several sheets if necessary)				APPLICANT(S) Hatsuki KANBAYASHI				•
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		U	S. PAT	ENT DOCU	JMENTS			
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